

Arav Raghunathan

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EDUCATION

Georgia Institute of Technology (GT), Atlanta, GA Expected in May 2028
Bachelor of Science in Electrical Engineering.
Notable Coursework: Digital System Design, Signal Processing, Programming for Hardware/Software Systems
Current Standing: Junior

WORK EXPERIENCE

Oracle Corporation, *Software Development Intern*, Suwanee, GA August 2024 – June 2025

- Executed collaborative software development projects utilizing Oracle APEX, PL/SQL, SQL, and Python.
- Managed project-based tasks for 15+ juniors and seniors, ensuring organization and clarity.

iCode Alpharetta, *Course Instructor*, Alpharetta, GA June 2024 – June 2025

- Accumulated 100+ hours of teaching experience in educational settings.
- Worked with students to foster interest and teach fundamentals in science and engineering concepts.

PROJECTS

MindGuard June 2024 – September 2024

- Engineered an app to enhance psychological wellness and optimize task management.
- Leveraged Python OOP and SQLite integration to formulate efficient framework.

Number Converter January 2022 – June 2022

- Designed an app for inter-system numeric conversion.
- Developed expertise in the KivyMD API and Python modularity to ensure effective implementation.

Automated Spray Gun Pyrolysis December 2025 – Present

- Developing an automated spray gun system for applying coatings to solar cells.
- Creating mechanical components in Onshape CAD and implemented control logic using Arduino.

ACTIVITIES

ICE Lab @ Georgia Tech, *Undergraduate Researcher* December 2025 – Present

- Assisting in the synthesis of FPAA's in SkyWater's 130 nm CMOS process.
- Collaborating with researchers to support analog hardware development and verification.

The Hive Makerspace, *Peer Instructor* January 2026 – Present

- Advising 1000+ students in hands-on technical skills, including breadboarding, laser cutting, and PCB fabrication.
- Programming social and community service events to promote electrical engineering education.

SiliconJackets, *ASIC Physical Design Engineer* January 2026 – Present

- Developed a 32-bit RTL calculator with custom datapath design and SRAM-based result storage.
- Supporting ASIC tapeout workflows as part of the Physical Design team.

FRC Team #9477, *Chief Engineer / Lead Designer* August 2023 – May 2025

- Directed computer-aided and mechanical design activities of a robotics competition team of 80+ members.
- Developed robot designs helped win state and regional competitions in the 2024 and 2025 competitive seasons.

SKILLS

Technical Skills: Python, Java, C, RISC-V, SQL, SystemVerilog, Linux, Analog Design, Digital Design, LVS, Magic, XSCHEM, Embedded Systems (Arduino), Breadboarding, Waveform Analysis, Cadence, FPGA / FPAA Design, ASIC Design, Onshape
Interpersonal Skills: Critical Thinking, Communication, Leadership, Collaboration, Diligence, Organization, Entrepreneurship